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Page : 2

Attorney's Docket No.: 14603-016US1  
Client's Ref.: P2003,0256USN

**AMENDMENTS TO THE SPECIFICATION:**

Please delete the heading "DESCRIPTION" at page 1, line 1.

Please amend the title at page 1, line 3:

Flip-Flop Circuit ~~Arrangement~~

Please add the following centered heading at page 1, line 4:

TECHNICAL FIELD

Please add the following centered heading at page 1, line 7:

BACKGROUND

Please add the following centered heading at page 1, line 28:

SUMMARY

Please add the following centered heading at page 6, line 10:

DESCRIPTION OF THE DRAWING

Please add the following centered heading at page 6, line 15:

DETAILED DESCRIPTION

Please replace the Abstract on page 14 with the following new Abstract:

A flip-flop circuit includes a first differential amplifier with first emitter-coupled transistors having emitters connected to a first emitter node, where the first emitter-coupled transistors include collector terminals that form at least parts of a first circuit node and a second circuit node, and base terminals that are cross-connected to collector terminals of the first emitter-coupled transistors. A second differential amplifier includes second emitter-coupled transistors having emitters connected to a second emitter node, where the second emitter-coupled transistors include collector terminals that are connected to the first circuit node and/or to the second circuit node, and base terminals that form at least part of a third circuit node and a fourth circuit node. A third differential amplifier includes third emitter-coupled transistors having emitters connected to the second emitter node, where the third emitter-coupled transistors include collector terminals that are connected to the third circuit node and/or to the fourth circuit node, and base terminals that are cross-connected to collector terminals of the third emitter-coupled transistors. A fourth differential amplifier includes fourth emitter-coupled transistors having emitters connected to the first emitter node, where the fourth emitter-coupled transistors include collector terminals that are connected to the third circuit node and/or to the fourth circuit node, and base terminals that are connected to the second circuit node and/or to the first circuit node;

Please delete the word "Figure" at page 14, line 17.